



Fab notes:

1. Material: 0.063" FR-4 glass epoxy, 1 oz copper
2. This is a 2-layer PCB
3. Route to indicated dimensions +/- 0.01"
4. Minimum lines 0.024", spaces 0.010"
5. Soldermask over bare copper, green color, both sides
6. Remove silkscreen from soldermask opening areas
7. Silkscreen legend comp side, white
8. All holes plated through 0.001" minimum unless specified
9. Hole diameters after plating, +/- 0.004"
10. Minimum plated hole size 0.024"
11. May change silkscreen width per process requirements
12. May change soldermask openings per process requirements

DRILL DRAWING

DRILL PLAN

SYM	SIZE	TOL.	CNT	PLATE
*	0.024 in	+/-0.004 in	35	Yes
X	0.032 in	+/-0.004 in	32	Yes
◇	0.036 in	+/-0.004 in	40	Yes
◇	0.040 in	+/-0.004 in	208	Yes
⊗	0.047 in	+/-0.004 in	8	Yes
◻	0.050 in	+/-0.004 in	132	Yes
◇	0.052 in	+/-0.004 in	16	Yes
◇	0.055 in	+/-0.004 in	21	Yes
⊗	0.076 in	+/-0.004 in	62	Yes
⊗	0.150 in	+/-0.004 in	9	No
TOTAL			563	554

Drill file will contain one extra of each drill, placed in above diagram.