



DRILL PLAN				
SYM	SIZE	TOL.	CNT	PLATE
⊘	0.032 in	+/-0.004 in	28	Yes
⊘	0.040 in	+/-0.004 in	28	Yes
⊘	0.044 in	+/-0.004 in	8	Yes
⊘	0.047 in	+/-0.004 in	7	Yes
⊘	0.050 in	+/-0.004 in	10	Yes
⊘	0.100 in	+/-0.004 in	2	Yes
⊘	0.150 in	+/-0.004 in	4	0

TOTAL 87 83

Drill file will contain one extra of each drill, placed in above diagram.

TOP SILKSCREEN DRILL DRAWING  
ASSY DWG

Fab notes:

1. Material: 0.063" FR-4 glass epoxy, 1 oz copper
2. This is a 2-layer PCB
3. Route to indicated dimensions (in mm) +/- 0.1mm
4. Minimum lines 0.016", spaces 0.008"
5. Soldermask over bare copper, red color, both sides
6. Remove silkscreen from soldermask opening areas

7. Silkscreen legend comp side, white
8. All holes plated through 0.001" minimum, unless specified
9. Hole diameters after plating, +/- 0.004"
10. Minimum plated hole size 0.032"
11. May change silkscreen width per process requirements
12. May change soldermask openings per process requirements